



**NATIONAL INSTITUTE OF TECHNOLOGY, ROURKELA – 769 008**  
**SPONSORED RESEARCH, INDUSTRIAL CONSULTANCY & CONTINUING EDUCATION**

NITR/SR/2020/Advt-19EC-073/L/016 dt. 05.11.2020

Applications are invited on prescribed format (On-line) for the temporary post of "**Project Assistant**" for the Project (Code: SR/19/EC/073) entitled "**Development of a Closed-loop Integrated MEMS Capacitive Accelerometer for Inertial and Navigation Systems**" Selected candidate may enroll in Ph. D programme in the **Department of Electronics & Communication Engineering, National Institute of Technology Rourkela** based on Institute norms and regulations.

1.	Project Code:	SR/19/EC/073
2.	Name of the Temporary Post:	Junior Research Fellow - 02 Posts
3.	Name of the Research Project:	<b>Development of a Closed-loop Integrated MEMS Capacitive Accelerometer for Inertial and Navigation Systems</b>
4.	Name of the Sponsoring Agency:	SERB, DST
5.	Last Date and Tenure of the Project:	03-March-2023
6.	Tenure of the Assignment:	till the end of project as mentioned in S. No. 05.
7.	Job Description:	The project students (likely to be converted as PhD students) will be involved in the design of analog integrated circuits for sensor interfacing and MEMS accelerometer sensor design. They have to work on Cadence for circuit simulation for analog integrated circuit and implementation of SC based circuits, amplifier, filter, sigma-delta ADC etc. to form a complete interfacing ASIC and COMSOL/IntelliSuite/CoventorWare for MEMS design and simulation. MEMS and ASIC to be fabricated abroad and will be integrated together for testing and characterization of the complete system.
8.	Consolidated monthly Salary/ Fellowship:	INR 31,000/- per month for 1st and 2nd year. INR 35,000/- per month for 3rd year.
9.	Essential Qualifications and experience:	M.E. / M. Tech. in Instrumentation / Electronics / Electrical with specialization in VLSI / Instrumentation / Electronics or similar fields with above 65% (7 CGPA) of marks <b>OR</b> B.E. / B. Tech in Instrumentation / Electronics or similar fields with above 9.0 CGPA and valid GATE score.
10.	Desirable Qualifications/ Experiences:	Experience on analog circuit design, MEMS design and familiarization with any MEMS and IC design tool is desirable. GATE score will be preferred.
11.	Selection Process:	Interview (Practical test if required)
12.	Accommodation:	Bachelor accommodation in the Institute may be provided and campus accommodation may be provided to married students subject to availability.

For technical information on the project, the candidate may contact the Principal Investigator at the following address **with an updated Curriculum Vitae**:

Name : **Prof. Sougata Kumar Kar**  
Address : Assistant Professor, Department of Electronics & Communication Engineering  
National Institute of Technology, Rourkela, Odisha, 769 008.  
Tel. No. : 0661-246 2473 (O)  
Mobile : +91- 94332 80223  
E-mail : [kars@nitrkl.ac.in](mailto:kars@nitrkl.ac.in)

Date and Time for **On-line** Interview: **03<sup>rd</sup> December, 2020 at 10:30 AM**

Place of Interview: **Department of Electronics & Communication Engineering, NIT Rourkela**

The candidates are required to send the complete filled up application form (soft copy) to [kars@nitrkl.ac.in](mailto:kars@nitrkl.ac.in). The application form is available in the following link [http://nitrkl.ac.in/oldwebsite/Jobs\\_Tenders/5ProjectFellowships/Doc/JRF%20LS-PND-64\(2\).pdf](http://nitrkl.ac.in/oldwebsite/Jobs_Tenders/5ProjectFellowships/Doc/JRF%20LS-PND-64(2).pdf) which must be filled by candidates and also required to attach photocopies of all supporting documents, research papers (if any) etc.

Candidates will build **a single PDF**; it is to be sent to Principal Investigator ([kars@nitrkl.ac.in](mailto:kars@nitrkl.ac.in)) **on or before 23.11.2020**. The candidates are also required to produce relevant documents mentioned in the application form (such as original of all mark sheets and certificates, research papers (if any), experience certificate (if any) etc. during interview.)

Mere possession of minimum qualification does not guarantee invitation to the interview. Candidates will be short listed based on merit and need of the project.

**Sd/-  
Asst. Registrar (SRICCE)**

Copy to:

- 1) All Heads of the Departments, NIT Rourkela for publication on Departmental Notice Boards.
- 2) Prof. Sougata Kumar Kar, Principal Investigator with a request to give wide publicity to advertisement.
- 3) Head of the Department, Electronics & Communications Engineering.
- 4) Project file.